

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,846,713 B1  
DATED : January 25, 2001  
INVENTOR(S) : Da-Soon Lee

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page.

Item [57], **ABSTRACT.**

Line 4, after "methods comprises the" please delete "steps of forming" and insert -- steps of: forming -- in its place.

Line 11, after "of the select" please delete "transistor, after" and insert -- transistor; after -- in its place.

Line 13, after "poly-1" please delete "layer, forming" and insert -- layer, forming -- in its place.

Column 4,

Line 62, after "select" please delete "transistor," and insert -- transistor; -- in its place.

Line 67, after "of a cell" please delete "transistor," and insert -- transistor; -- in its place.

Signed and Sealed this

Fifth Day of July, 2005



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*